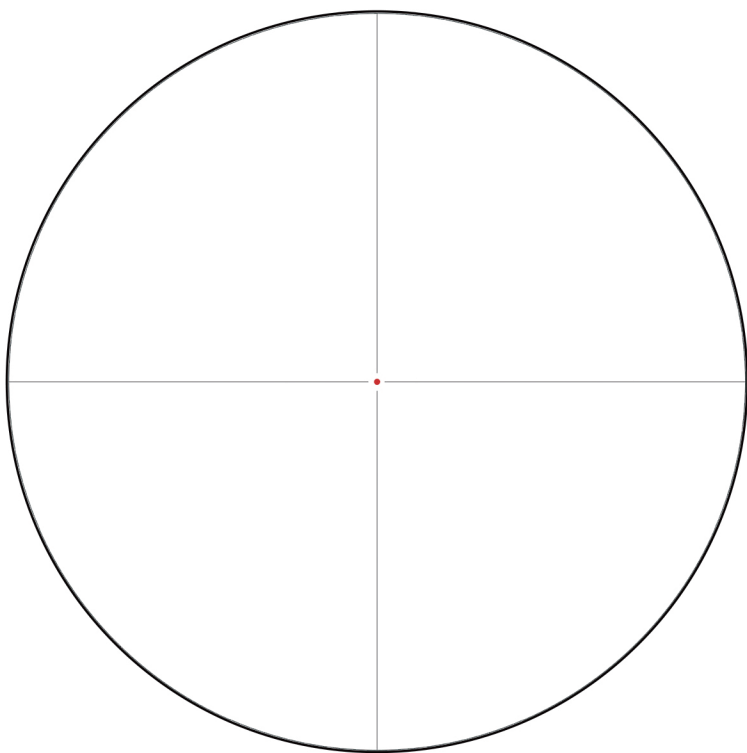


VETD-LR



VEMP-LR

